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Art Unit 2121

Re: U.S. Patent No. 7,139,631; Issued: November 21, 2006
For: **Method and System to Compensate for Scanner System
Timing Variability in a Semiconductor Wafer Fabrication System**
Inventor: Hilario OH
Our Ref: 1857.6890001

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Power of Attorney to Prosecute Applications Before the USPTO; and
2. Statement Under 37 C.F.R. 3.73(b).

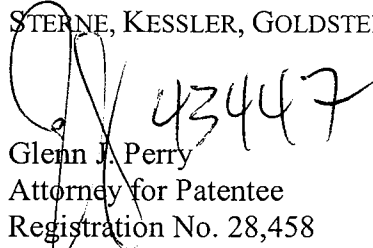
The above-listed documents are filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.


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GJP/lvt
Enclosure(s)

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